



WP-2008: Noise and Signal Distortion Considerations

When the electrical signals leave the encoder, they are “clean” in the sense of being noise free. However, by the time these signals reach their intended destination they can be degraded by radiated and induced electrical noise, as well as signal distortion mechanisms such as cable capacitance and impedance mismatches. Inadequate shielding, poor cable termination, poor cable quality, and long cable lengths all contribute to undesirable signals.



If cable lengths are in excess of 9.1 Meters., use of differential line driver outputs along with differential type receiver circuitry is recommended.

All cables have small amounts of capacitance between adjacent conductors. This capacitance is a direct function of the cable’s length, and tends to round off the leading edge of the square wave signal, increasing rise times. It can also distort the signal to the extent of causing errors in the system.

To minimize the distortion, it is recommended that low capacitance cable be used (less than 30 picofarads per foot), and that cable lengths should be as short as possible. Signal distortion is not usually significant for lengths less than 9.1 Meters. (or 1000 picofarads).

A low capacitance twisted-shielded pair cable should be used whenever using differential signals, with cable lengths in excess of 9.1 Meters. For high frequency applications (>200kHz), this type of cable may be needed for all lengths.

Proper cable termination is extremely important with differential signals. First, try an un-terminated configuration. However, signal reflections can occur, resulting in severely distorted waveforms. If signal distortion occurs, parallel termination, which involves placing a resistor across the differential lines at the far (receiver) end of the line is recommended. This resistor should be approximately equivalent to, or up

to 10% greater than, the characteristic impedance of the cable (Z_0) [usually between 70-150 ohms]. This permits higher frequencies to be transmitted without significant distortion. Unfortunately, low valued resistors can increase the power dissipated by the line driver, and reduce the output signal swing. In this case, a capacitor should be placed in series with the resistor. The capacitor value should be equal to the round trip delay of the cable divided by the cables Z_0 . Round trip delay is equal to the cable length multiplied by $1.7\text{ns}/0.3\text{M}$. Note that the RC time constant of this type of termination can reduce the system frequency response.

A parallel termination resistor of a larger value than given above can often provide adequate reduction of signal reflections, and still maintain adequate frequency response, and low power dissipation. Experimentation is often required for each application consisting of long cable runs and high frequencies.

It is extremely important to connect cable shields to ground on the instrument end (counter, PLC, etc...). Always properly ground the motor/machine for which the encoder is mounted. Also ground the encoder case under the following conditions:

- (1) **DO NOT** ground the encoder case through both the motor/machine and the cable wiring, and
- (2) **DO NOT** allow the encoder cable wiring to ground the motor/machine exclusively. High motor/machine ground currents could flow through encoder wiring, potentially damaging the encoder and associated equipment.